FIG. 1

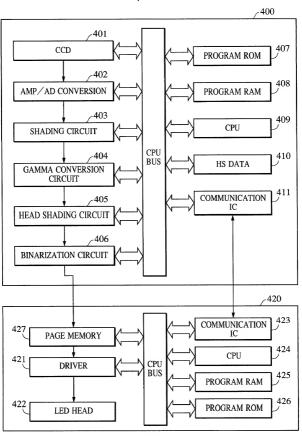
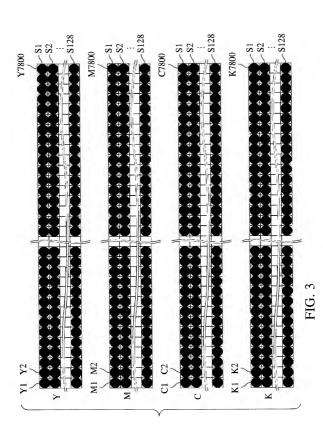
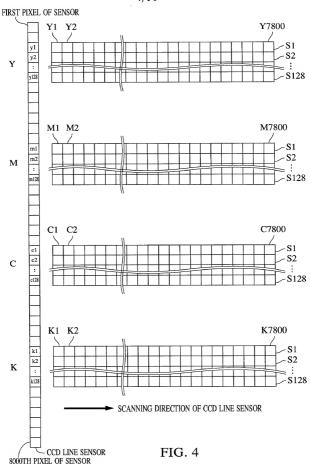


FIG. 2





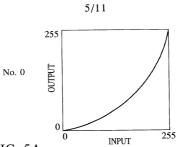


FIG. 5A

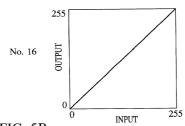


FIG. 5B

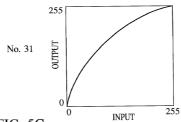


FIG. 5C

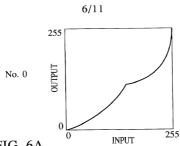


FIG. 6A

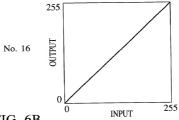


FIG. 6B

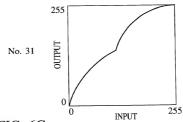


FIG. 6C

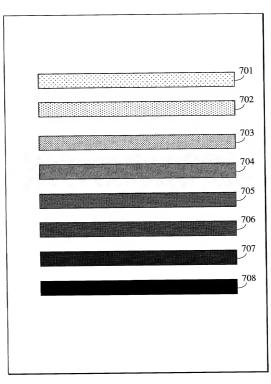


FIG. 7

a

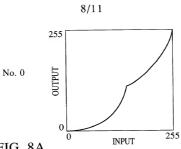
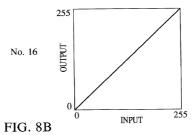
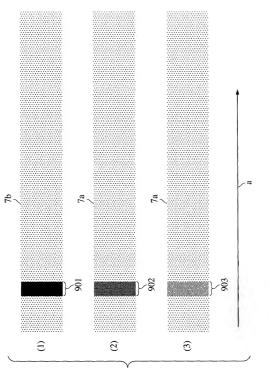


FIG. 8A



255 OUTPUT No. 31 255 INPUT

FIG. 8C



COMMUNICAL CHECK

0 51

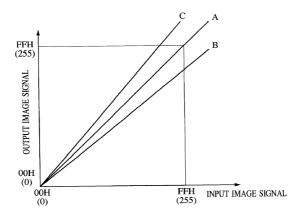


FIG. 10 PRIOR ART

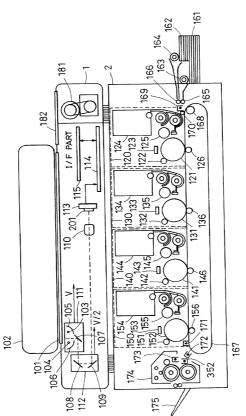


FIG. 11